

USB4 1.0 ENGINEERING CHANGE NOTICE FORM

Title: Clarification about TMU on Low Power States – V2
Applied to: USB4 Specification Version 1.0

Brief description of the functional changes:

Clarification about the requirement to transmit Delay Response. V2 changed the wording and add it to Adaptive Uni-directional mode.

Benefits as a result of the changes:

Clear definition about when it is required to transmit the Delay Response

An assessment of the impact to the existing revision and systems that currently conform to the USB specification:

None

An analysis of the hardware implications:

None

An analysis of the software implications:

None

An analysis of the compliance testing implications:

None

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Actual Change

(a). 7.3.1.2 Uni-Directional Time Sync Handshake

To Text:

When the Link Transmitter is not in CL2, CL1 or CL0s Tx~~a Low Power state (CL0 or CL0s RX)~~, a Downstream Facing Port shall send a Delay Response to the Upstream Facing Port at the interval specified in the *TSPacketInterval* field in the TMU_RTR_CS_3 register in Router Configuration Space. A Downstream Facing Port shall transmit a Follow-Up Packet within SendTimeout time after transmitting the associated Delay Response Packet.

(b). 7.3.1.3.2 Adaptive Uni-Directional Mode (Ver. 2 Only)

To Text:

When the TransmitterAdapters on a Link are ~~is~~ not in a-CL2, CL1 or CL0s Tx state, the Downstream Facing Port shall send a Delay Response to the Upstream Facing Port at the interval specified in the AdapterTimeSyncInterval field in the TMU_ADP_CS_9 register in TMU Adapter Configuration Space.